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10/008696

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAH	EXAMINER
10008696	11/08/2001	717 711 710	227 240	2185	Rinehart K Dang

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2181
2182/11

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/249,083 11/15/2000

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** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claims: 35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
Verified and Acknowledged Examiner's initials	TI-30105	
TITLE : Multicore DSP device having coupled subsystem memory buses for global DMA access U.S.DEP'T. OF COMM./PAT. & TM-PTO-435L(Rev. 12-54)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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